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L6 AND "lead frame"

19

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DB=USPT; PLUR=YES; OP=OR

<u>L7</u>	L6 AND "lead frame"	19	<u>L7</u>
<u>L6</u>	(internal near3 connect\$4) AND (external) AND 15	97	<u>L6</u>
<u>L5</u>	(chip OR die) NEAR7 (window\$2)	2387	<u>L5</u>
<u>L4</u>	embedded NEAR memory NEAR assembly	3	<u>L4</u>
<u>L3</u>	embedded NEAR memory NEAR assembly	0	<u>L3</u>
<u>L2</u>	L1 NEAR assembly	1049	<u>L2</u>
<u>L1</u>	embedded memory	472613	<u>L1</u>

END OF SEARCH HISTORY